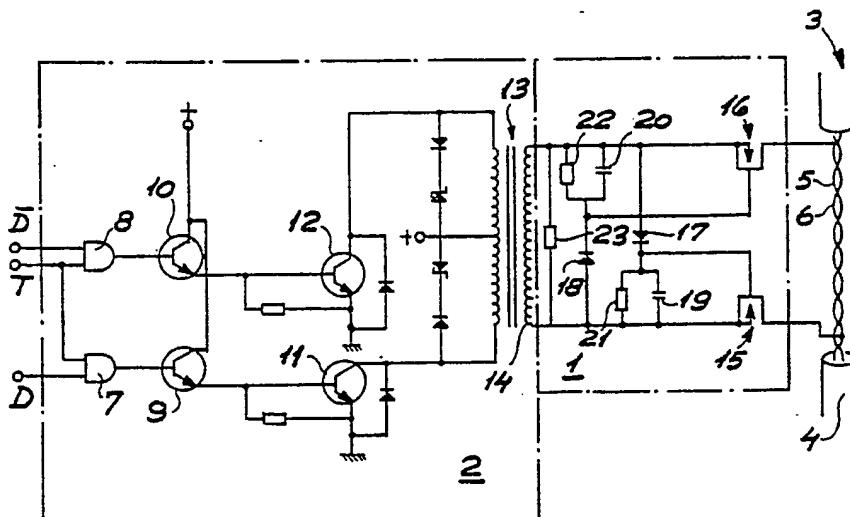




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(54) Title: COUPLING CIRCUIT FOR TRANSFERRING DATA SIGNALS AT A HIGH RATE



(57) Abstract

Coupling circuit (1) for transferring data from a data transmitter (2) to a bus (3) common to several transmitters so that each data transmitter is galvanically separated from the bus. The coupling circuit (1) has a switch (15, 16) connected in series between each associated input (26, 27) and output (28, 29), which switch via a rectifier circuit (17, 19, 21 and 18, 20, 22) in the coupling circuit (1) is controlled solely by the data signals transferred from the data transmitter (2) via separator means (14, 24, 25).

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Coupling Circuit for Transferring Data Signals
at a high rate

The invention relates to a coupling circuit for transferring data signals at a high rate from a data transmitter to a bus common to several data transmitters. The invention concerns in particular coupling circuits for data transmitters that may be placed far from each other, which entails that there may be huge differences in potential between the individual data transmitters, and it is therefore required that the data transmitters are galvanically separated from the long bus by separator means and that the data signal is transmitted on the bus in the form of a differential signal. More particularly, the invention relates to a coupling circuit for transferring data at a high rate from a data transmitter to a bus common to several data transmitters, said circuit comprising separator means for galvanically separating each data transmitter from the bus and for producing an output signal on a signal output via switch means positioned between the signal output and the separator means.

When a single one out of many data transmitters connected to the same bus is to transmit data signals, the non-transmitting units must not load the bus noticeably, and it is therefore necessary that beside having two logic transmission states where the associated generator impedance is low, each transmitter has also a third state,

called "high impedance state", where the transmitter has a high output impedance when it does not transmit data signals.

5 The separator means may be a transformer whose secondary windings constitute the terminals of the data output, or may be provided by means of capacitors, optical couplings, etc. The German Offenlegungsschrift 2 702 209 shows an example of optical separator means. They suffer from the drawback that they cannot transfer power and that the bus
10 must be active, e.g. carry supply current to the photo-transistors.

A transformer is preferred for most uses because, normally, higher signal amplitude is desired on the bus than the output voltage of most logic circuits, but for uses where
15 no increase in amplitude is required the separator means will be cheaper in the form of a pair of capacitors. Below the invention will mainly be described in connection with a transformer as separator means.

Coupling circuits are known where one winding of the trans-
20 former is directly connected to the bus, while the other winding of the transformer is connected to a driver which for providing the high impedance state exhibits high output impedance when no data signals are transmitted. This technique, however, can only be employed in connection with
25 high rate data signals if only few circuits of this type are connected to the common bus. The high output impedance in the high impedance state of the transmission circuit should of course be converted into a correspondingly high output impedance by the transformer, but owing to the
30 high transmission frequency the iron core of the transformer will result in a heavy load on the bus. For many uses, such as for multiplex systems where the units alternately transmit many brief data blocks, it is desired that the coupling circuit has a change-over time which is
35 significantly shorter than the change-over time for relays,

and consequently relay technique cannot be used for the purpose.

The object of the invention is to provide a rapidly reacting coupling circuit that can transfer data signals from a data transmitter to a bus common to many transmitters and which brings about an effective high impedance state, i.e. that many coupling circuits of the invention may be connected to a common bus without loading the bus noticeably when data signals are not transmitted.

10 This object is achieved in that the switch means comprise a switch which is connected in series between each signal output and the separator means and is arranged to be actuated and driven solely by the data signal applied from the data transmitter through the separator means. Thus

15 the circuit is interrupted at such a location that the iron core of the transformer or any other load via a separator capacitor does not involve the drawback mentioned above. The switches are actuated only by the data signal applied from the data transmitter through the separator

20 means, and therefore the desired, galvanic separation is maintained which could not be achieved e.g. by inserting drive transistors at the location of the switches without separate means for supplying the drive transistors with direct current. Thus, it is a characteristic feature of

25 the coupling circuit of the invention that it is capable of showing an effective high impedance state irrespective of the output impedance of the data transmitter and that it shall not be connected to a current supply.

Said switch is preferably a transistor device whose control

30 electrode is connected to a rectifier circuit coupled to the output of the separator means, where the control electrode does not result in any noticeable load.

The use of a field effect transistor simplifies considerably the transistor device because a field effect



transistor is bipolar and thus particularly expedient in connection with the differential operation where current flows in both directions.

5 Owing to the transmission effect such transistors are selected as show high resistance in the disconnected state and are capable of carrying a heavy current in the open state, which is the case e.g. of the so-called V-MOS field effect transistors.

10 If a semi-conductor, which does not exhibit infinitely large resistance in the disconnected state, is used as a switch a transmitter will produce a voltage across the rectified circuit of another transmitter for the transistor device if the separator means have a high output impedance in a non-transmitting state. Said voltage
15 will reduce the series resistance of the transistor device, resulting in an even higher voltage across the rectifier circuit so that the first-mentioned, transmitting unit will drive the coupling circuit of another unit in an open state. This is avoided by connecting a resistive load in parallel
20 across each rectifier circuit when the resistance of the resistive load is selected to be suitably lower than the resistance of the transistor devices in their disconnected state.

25 The coupling circuit of the invention thus permits the simultaneous achievement of a short change-over period, a relatively high transmission effect and a high impedance state irrespective of the construction of the data transmitter and the separator means. The coupling circuit is accordingly a well-defined, generally useful circuit with
30 few components that require no current supply. The coupling circuit is accordingly particularly useful for being integrated on a single semi-conductor substrate.

The invention will be explained in greater detail in the



following description of an embodiment with reference to the drawing, in which

fig. 1 shows an embodiment of the coupling circuit of the invention, connected between a bus and a transmission driver with a transformer as separator means, and

fig. 2 shows another transmission driver with capacitors as separator means.

In fig. 1 there is shown an embodiment of the coupling circuit 1 of the invention, located between a driver 2 known per se and a bus 3 consisting of a screen 4 and two twisted inner conductors 5 and 6 which are connected to their respective output terminals of the coupling circuit 1.

Before the description of the coupling circuit itself, the manner in which the driver 2 operates will first be described, particularly in relation to the so-called high impedance state which means that the output impedance of the driver 2 may be high depending upon a control signal T on the input of the driver. The driver 2 has two other inputs, D and \bar{D} , respectively, to which the data signals (and the conjugated signals) are applied which are to be transmitted on the bus 3. If the state of the input terminal T is logic one it will be appreciated that the data signals D and \bar{D} are transmitted directly through the AND gates 7 and 8, respectively, to alternately drive the transistors 9, 11 and 10, 12, respectively, in conductive and disconnected state, respectively, so that alternately current flows through the two primary windings of the transformer 13. The unspecified diodes in the figure serve, in a manner known per se, to protect the transistors 11, 12, while the unspecified resistors serve to properly bias the circuit in a manner known per se. If the state of the input terminal T is changed to logic zero no data will be passed through the AND gates 7 and 8 so that the transistors are not conductive. Therefore the driver 2 consumes

only little current when no data are transmitted, and in this state the output impedance of the driver 2, seen into the secondary winding 14 of the transformer 13, is high.

According to the invention the coupling circuit 1 is
5 designed to provide an interruption between the bus 3 and the transformer 13 in such a manner that this interruption may be controlled very quickly and in a galvanically separated manner relative to the driver 2. In the shown embodiment the interruption is effected by means of two
10 field effect transistors 15, 16 whose drain electrodes are connected to the respective conductors 5 and 6, respectively, and whose source electrodes are connected to the respective ends of the winding 14. The control electrode of the transistor 15 is connected to a rectifier circuit
15 comprising a diode 17 and a capacitor 19, while the control electrode of the transistor 16 is connected to a corresponding rectifier circuit comprising a diode 18 and a capacitor 20. When the driver 2 transmits data signals via the transformer 13 a control voltage will be produced in the
20 junctions between the diode and the capacitor 17, 19 and 18 and 20, respectively. The control voltage drives the field effect transistors 15, 16 in a conductive state where independently of the polarity they show a resistance of a few ohms so that the data signals are transmitted
25 directly to the bus 3. When the driver 2 does not transmit data signals no control voltage will be produced for the transistors 15 and 16 which are therefore in disconnected state where there is such a high resistance that the coupling circuit 1 involves no noticeable load on the bus 3.

30 The coupling circuit of the invention is particularly characteristic in that besides having the properties already mentioned it can change very quickly between the open (small resistance) and the closed (large resistance) state. If the data rate is e.g. of a magnitude 10^6 bit per
35 sec., it will only take a few bits for the transistors 15 and 16 to open. In practice, this may be done by providing

a brief bit pattern to precede the data block it is desired to transmit to the bus 3 via the coupling circuit 1, said bit pattern resulting in a sufficient number of changes in polarity depending upon the data representation so that the coupling circuit is in the fully open state when the flag of the data block is to be transmitted. On termination of the data transmission the coupling circuit changes quickly to the closed state depending upon the time constant selected for the capacitor 19 and a resistor 21 and the capacitor 20 and a resistor 22, respectively. The time constant is selected so that the coupling circuit may be kept open sufficiently long depending upon the polarity change of the selected data representation.

As the impedance, seen into the winding 14 of the transformer 13, is sufficiently high, as explained in the foregoing, when no data are transmitted, it will be appreciated that a data signal which is transmitted on the bus 3 by another unit with the coupling circuit 1 in the closed state, will be able to produce a voltage across the winding 14 because the resistance of the transistors 15 and 16 is not infinitely high in the closed state. This involves the risk of the voltage across the winding become so high that the transistors 15 and 16 open which results in an additional increase in the voltage across the winding 14 so that eventually a transmitting unit will drive the other coupling circuits connected to the bus 3 to the open state, which in turn will apply a detrimental load to the transmitting unit. This phenomenon is obviated by a resistor 23 which is mounted across the winding 13 and whose resistance is so determined with respect to the resistance of the transistors 15, 16 in their disconnected states that the control voltage cannot exceed a predetermined value.

In the shown embodiment the control voltages for the transistors 15 and 16 are provided by rectifier circuits which are directly connected across the winding 14 of the



transformer 13. It will be appreciated that a lower or higher control voltage may be provided by means of a special tap on the winding 14 or by means of separate windings. The construction of the coupling circuit shown in the figure has the advantage that it has only two input terminals that can be connected directly to the winding 14 so that the coupling circuit of the invention is particularly suitable for being manufactured as an integrated circuit which is not to be connected to the current supply, but has merely two input terminals and two output terminals for coupling between the transformer and the bus.

The general use of the circuit appears also from the fact that it carries no importance what separator means are used in connection with the data transmitter. The circuit shown in fig. 2 will serve as an example of a data transmitter with separator capacitors and will be briefly explained.

Fig. 2 shows a data transmitter with separator means in the form of two capacitors 24 and 25, which are connected to their respective input terminals 26 and 27, respectively, of the coupling circuit 1 which is shown in fig. 1 and whose output terminals 28 and 29 can be connected to the bus 3 shown in fig. 1. Like the data transmitter shown in fig. 1 the data transmitter has two AND gates 30, 31 with inputs for a control signal T and for data signals D and \bar{D} . When data are transmitted the transistors 32 and 33 will alternately be conductive and apply data signals to the coupling circuit 1. This will be appreciated e.g. by assuming that the transistor 32 changes from a disconnected to a conductive state which means that a direct current is drawn through the resistor 34 and also a current pulse through the resistor 35, through the capacitor 25, the coupling circuit 1 and the capacitor 24. These current pulses drive the transistor devices in the coupling circuit 1 to the open state, transferring the data signals to the bus. When the control signal T is logic zero, no data

signals are transferred and the output impedance of the data transmitter, seen from the terminals 26 and 27, will be substantially equal to the parallel resistance of the resistors 34 and 35 at the signal transmission frequency.

- 5 As, however, no data are transmitted the transistor devices will be disconnected so that the output impedance on the terminals 28 and 29 is high, as is desired, irrespective of the resistance of the resistors 34 and 35. The data transmitter shown in fig. 2 has the drawback that
- 10 power is lost in the resistors 34 and 35 when data are transmitted, and the resistors 34 and 35 may therefore advantageously be replaced by transistor elements, but on the other hand the example shows that the coupling circuit of the invention also operates in cases where the data
- 15 transmitter is very simple and does not per se exhibit a high impedance state.

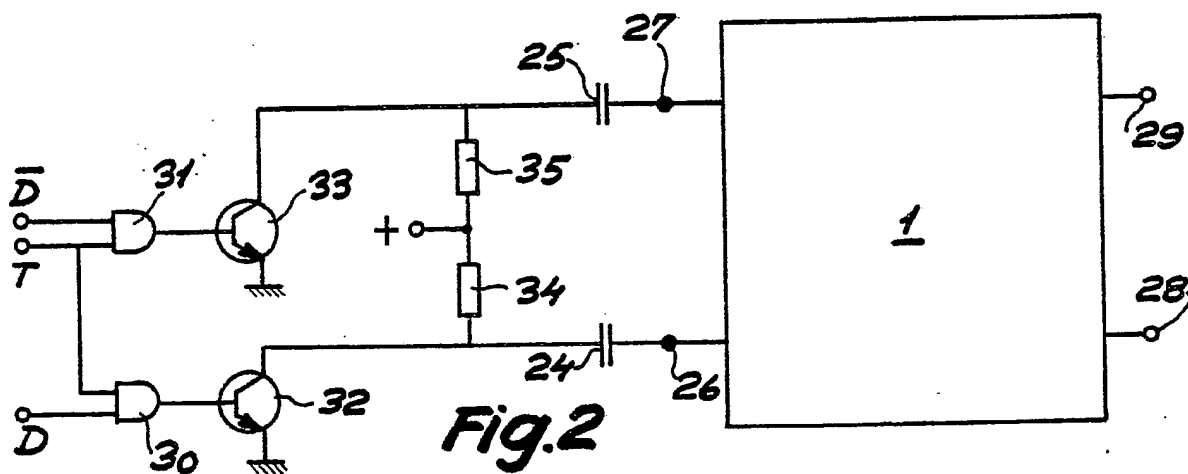
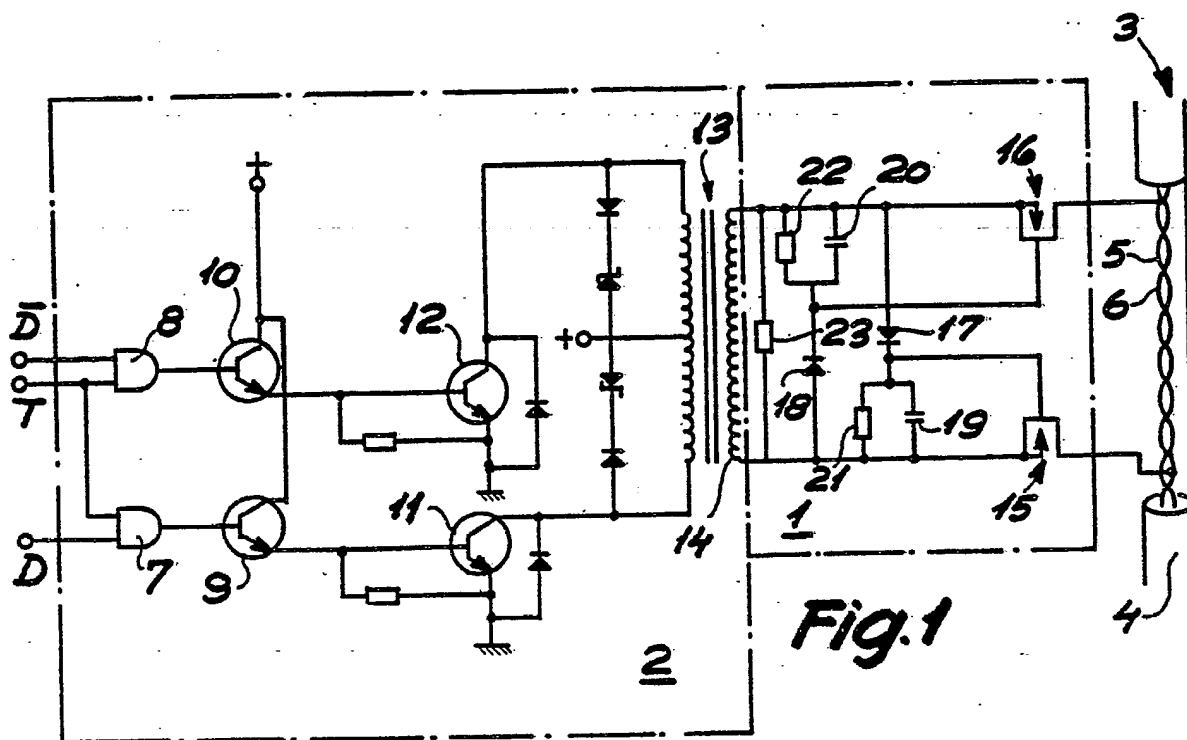
P a t e n t C l a i m s

1. A coupling circuit for transferring data at a high rate from a data transmitter to a bus common to several data transmitters, said circuit comprising separator means for galvanically separating each data transmitter from the bus and for producing an output signal on a signal output via switch means positioned between the signal output and the separator means, c h a r a c t e r i z e d in that the switch means comprise a switch (15, 16) which is connected in series between each signal output (28, 29) and the separator means (13, 24, 25) and is arranged to be actuated and driven solely by the data signal applied from the data transmitter (2) through the separator means.
2. A coupling circuit according to claim 1, c h a r a c - t e r i z e d in that the switch is composed of two transistor devices (15, 16) which with mutually opposite forward directions are mounted in series with said signal output and which each have a control electrode connected to an associated rectifier circuit (17, 19, 21 and 18, 20, 22) mounted in parallel across the separator means and galvanically separated from the data transmitter.
3. A coupling circuit according to claim 2, c h a r a c - t e r i z e d in that each transistor device (15, 16) is a field effect transistor whose source-drain electrode is connected in series with the respective signal output and whose control electrode is connected to the respective rectifier circuit.
4. A coupling circuit according to claim 2 where the separator means exhibit a high impedance state in an operative state, c h a r a c t e r i z e d in that a resistive load (23) is mounted in parallel across each rectifier circuit.

5. A coupling circuit according to claims 1, 2, 3 or 4, characterized in that the coupling circuit is integrated on a single semi-conductor substrate.

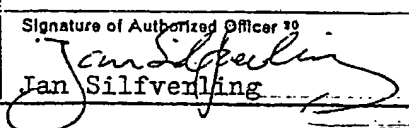


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INTERNATIONAL SEARCH REPORT

International Application No PCT/DK79/00043

I. CLASSIFICATION OF SUBJECT MATTER (If several classification symbols apply, indicate all) ³				
According to International Patent Classification (IPC) or to both National Classification and IPC 3				
G 06 F 3/04				
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Category ⁶	Citation of Document, ¹⁵ with indication, where appropriate, of the relevant passages ¹⁷	Relevant to Claim No. ¹⁸		
X	DE, A1, 2 702 209 published 1978, July 27, Licentia Patent-Verwaltungs-GmbH	1-3		
X	DE, A1, 2 101 211 published 1971, August 12, International Business Machines Corp.	1-3		
X	FR, A, 1 590 158 published 1970, May 22, see figure 8, Chezlemas René	1-3		
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